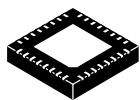


# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®

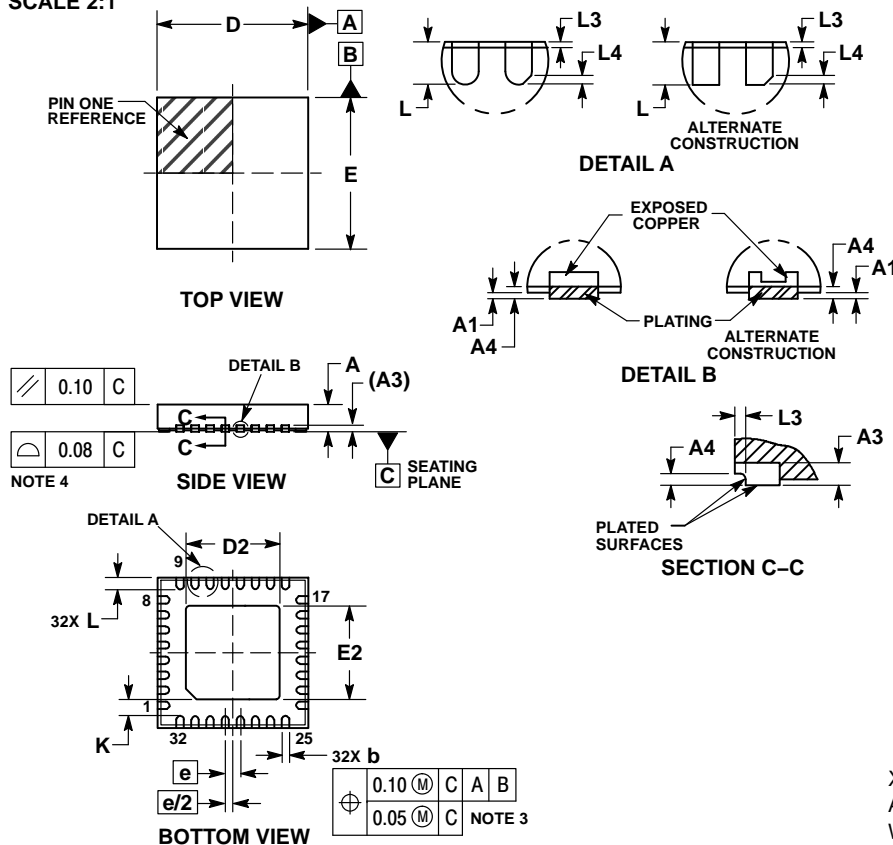


1 32

SCALE 2:1

QFNW32 5x5, 0.5P  
CASE 484AB  
ISSUE D

DATE 07 SEP 2018

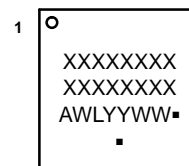


**NOTES:**

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.10 AND 0.20MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

| MILLIMETERS |          |      |      |
|-------------|----------|------|------|
| DIM         | MIN      | NOM  | MAX  |
| A           | 0.80     | 0.90 | 1.00 |
| A1          | ---      | ---  | 0.05 |
| A3          | 0.20 REF |      |      |
| A4          | 0.10     | ---  | ---  |
| b           | 0.20     | 0.25 | 0.30 |
| D           | 4.90     | 5.00 | 5.10 |
| D2          | 3.00     | 3.10 | 3.20 |
| E           | 4.90     | 5.00 | 5.10 |
| E2          | 3.00     | 3.10 | 3.20 |
| e           | 0.50 BSC |      |      |
| K           | 0.35     | ---  | ---  |
| L           | 0.30     | 0.40 | 0.50 |
| L3          | ---      | ---  | 0.10 |
| L4          | 0.08 REF |      |      |

**GENERIC MARKING DIAGRAM\***

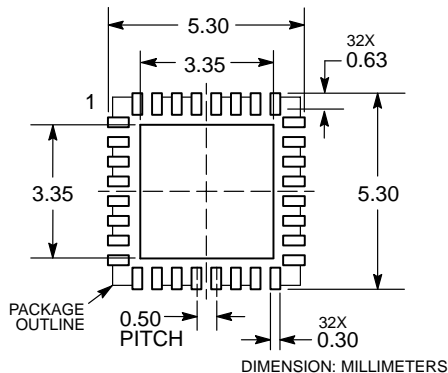


- XXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "C" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

**RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

|                         |                         |  |
|-------------------------|-------------------------|--|
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| <b>DESCRIPTION:</b>     | <b>QFNW32 5x5, 0.5P</b> | <b>PAGE 1 OF 1</b>   |

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