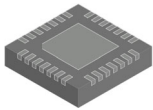


MECHANICAL CASE OUTLINE

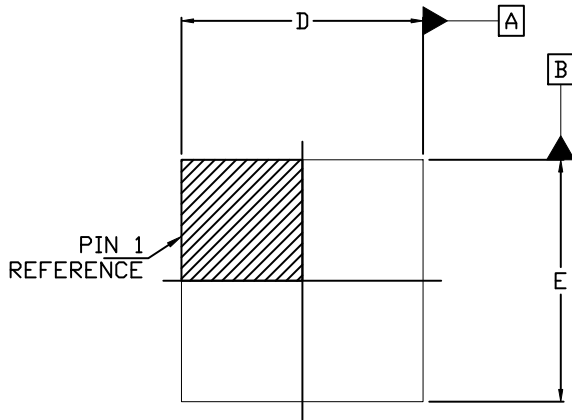
PACKAGE DIMENSIONS

ON Semiconductor®



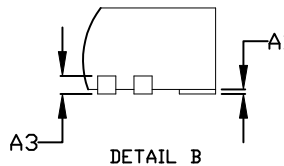
QFN28 4x4, 0.4P
CASE 485GF
ISSUE O

DATE 03 NOV 2020



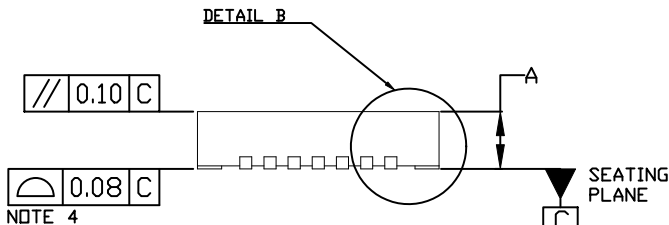
TOP VIEW

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
 2. CONTROLLING DIMENSION: MILLIMETERS
 3. DIMENSION *b* APPLIES TO PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.30MM FROM THE TERMINAL TIP.
 4. COPLANARITY APPLIES TO THE TERMINALS.
 5. POSITIONAL TOLERANCE APPLIES TO ALL TERMINALS.
 6. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

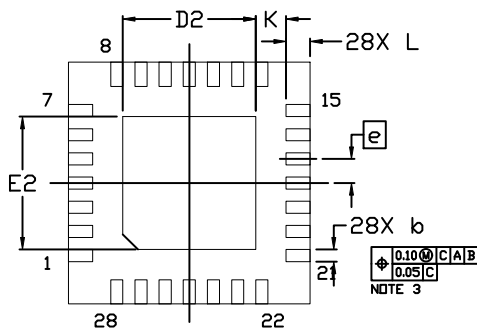


DETAIL B

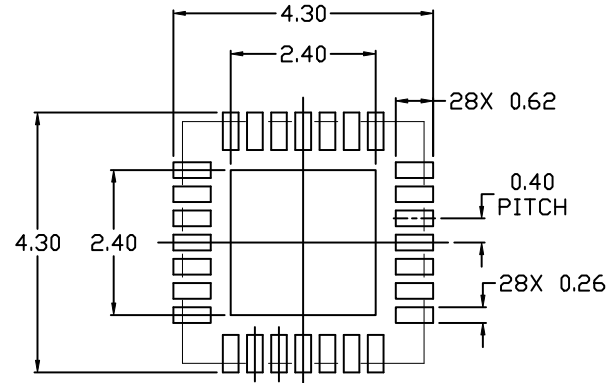
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.90	1.00
A1	0.00	---	0.05
A3	0.20 REF		
b	0.15	0.20	0.25
D	3.90	4.00	4.10
D2	2.10	2.20	2.30
E	3.90	4.00	4.10
E2	2.10	2.20	2.30
e	0.40 BSC		
K	0.50 REF		
L	0.30	0.40	0.50



SIDE VIEW



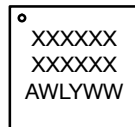
BOTTOM VIEW



RECOMMENDED MOUNTING FOOTPRINT*

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
Y = Year
WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	QFN28 4x4, 0.4P	PAGE 1 OF 1

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